

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Yamazaki, et al. Art Unit : 2775
 Serial No.: 09/504,235 Examiner : David L. Lewis
 Filed : February 15, 2000
 Title : METHOD OF MANUFACTURING A SEMICONDUCTOR DEVICE

#11

Commissioner for Patents
 Washington, D.C. 20231

TRANSMITTAL LETTER AND PETITION FOR AUTOMATIC EXTENSION

Correspondence relating to this application is enclosed.
 The required fees are computed below. Please apply any charges
 not covered, or any credits, to Deposit Account No. 06-1050.

Total						
Claims	67	-	54	=	13	\$234
Independent	13	-	13	=	0	\$0

Applicant hereby petitions under 37 C.F.R.
 \$1.136 for a 1 month extension of time. \$110

TOTAL FEE DUE \$344

A check for \$344 is attached.

Respectfully submitted,

Date: 1/28/02



James T. Hagler
 Reg. No. 40,631

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CERTIFICATE OF MAILING BY FIRST CLASS MAIL
 I hereby certify under 37 CFR §1.8(a) that this
 correspondence is being deposited with the
 United States Postal Service as first class mail
 with sufficient postage on the date indicated
 below and is addressed to the Commissioner for
 Patents, Washington, D.C. 20231.

Date of Deposit 1/28/2002

Signature Deborah K. Sim

Typed or Printed Name of Person Signing
 Certificate Deborah K. Sim